Fast and Secure Encryption System Based on a Chaotic Map

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Abstract
In this paper we propose a new fast and secure stream cipher based on a Modified Logistic Map and a Linear Feedback Shift Register.

Introduction
In the last years, it has become necessary to encrypt high amounts of data in real time. Unfortunately, most of the ciphers that are used nowadays are unable to encrypt data at high speed or are not secure enough.

In this context, chaos-based cryptosystems and, specially, chaotic maps, have emerged as a promising alternative to classical encryption since they are expected to achieve a good balance between speed and security [1].

In this paper, we propose and analyze a secure communication system based on a combination of a Modified Logistic Map (MLM) and a Linear Feedback Shift Register (LFSR).

Modified Logistic Map
The classic logistic map is expressed by the following equation:

\[ f(x_i) = x_{i+1} = \gamma x_i (1 - x_i), \quad x \in [0,1] \] (1)

This map presents chaos for most values of \(3.57 < \gamma < 4\) while, for \(\gamma > 4\), most initial values leave the interval \([0,1]\) and diverge.

However, this map has some issues that prevent it from being implemented directly in cryptosystems. First, there is an open and dense set of parameters \(\gamma\) for which the map is regular (i.e. there exist periodic windows). It is necessary to avoid using these parameters in order to generate truly chaotic sequences. Furthermore, the images are not uniformly distributed in the interval \([0,1]\). Instead, it can be easily proved that the images are distributed in the interval \([0, \gamma/4]\). Therefore, by measuring the length of the attractor, the value of \(\gamma\) can be determined. This can be a problem if the value of \(\gamma\) is being used as part of the key.

In our work, we have used a Modified Logistic Map proposed in [2] that is capable of solving these issues.

The MLM is expressed mathematically as:

\[
\begin{align*}
x_{i+1} &= \begin{cases} 
\gamma x_i (1 - x_i) \pmod{1}, & x \in I_{\text{ext}} \\
\gamma x_i (1 - x_i) \pmod{1}, & x \in I_{\text{int}}
\end{cases}
\end{align*}
\] (2)

with \(I_{\text{ext}} \subseteq (0,1)\setminus I_{\text{int}}, I_{\text{int}} = [\eta_1, \eta_2], \eta_1 = (1/2) - \left[ \frac{\gamma}{4} \right] / \gamma \) and \(\eta_2 = (1/2) + \left[ \frac{\gamma}{4} \right] / \gamma \) where \(\lceil \gamma/4 \rceil\) is the greatest integer minor or equal to \(\gamma/4\).

Proposed algorithm
When the MLM is digitized, the sequence generated becomes periodic since the maximum number of possible different values of \(x_i\) is \(2^n\), where \(n\) is the number of bits. However, the period lengths of the sequences generated by the MLM are usually much shorter than this value, which results in poor randomness [3].

Our algorithm solves this problem by perturbing the orbits generated by the MLM with a Linear Feedback Shift Register (LFSR). For this purpose, the Least Significant Bit (LSB) of each \(x_i\) is XORed with a bit generated by a LFSR. The resulting sequence of bits is combined with the plaintext using an XOR gate. On the other hand, the perturbed \(\tilde{x}_i\) is used to generate the next number \(x_{i+1}\) using (2). It can be proved that, if the period \(P_2\) of the LFSR is a prime number, the period of the generated sequences \(P_w\) will be \(P_w \geq P_2\) [4].

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shown in Fig. 2.

**Results**

The proposed MLM-LFSR algorithm has been able to encrypt images efficiently as shown in Fig. 3. Furthermore, in order to analyze the security of this algorithm, the generated sequences have been subjected to the National Institute of Standards and Technology (NIST) SP 800-22 battery of test.

The sequences have passed all the tests proving that our system is secure (Fig. 4).

**Conclusions**

A new encryption algorithm based on a MLM and an LFSR has been proposed. The sequences generated by this algorithm have passed the NIST randomness test, proving that this system is secure.

Currently, this system is being implemented in a Zedboard development board that includes a Zynq-7020 SoC. The implementation results will be presented in a future paper.

**REFERENCES**


