A Programmable Differential Transimpedance Amplifier for Capacitive MEMS Accelerometers

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Abstract

In this work, a fully-differential transimpedance amplifier with programmable gain and bandwidth for MEMS accelerometers is presented. It is aimed for a differential surface-micromachined combfinger capacitive accelerometer, but can be used in many other capacitive sensor applications.

Introduction

MEMS capacitive sensors have become very popular over the last years, since they offer excellent noise performance, low consumption, and a huge range of applications, from the automotive industry to videogames and electronic devices such as smartphones or tablets. One of the main uses of these sensors is the capacitive accelerometer. As shown in Fig. 1, the acceleration causes a displacement of a proof mass that produces a small change in the capacitance between it and fixed electrodes [1]. displacement is measured by stimulating the sensor with an ac voltage signal Vc. If the frequency of the carrier signal is much higher than the capacitance variations and the mechanical resonance, the current through the capacitors can be approximated by:

$$i_1 = [C_0 + \Delta C(t)] \frac{\partial V_C}{\partial t} \tag{1}$$

$$i_2 = [C_0 - \Delta C(t)] \frac{\partial V_C}{\partial t} \tag{2}$$

where C_0 is the nominal capacitance and the capaticance variation $\blacksquare C$, which is proportional to the displacement, can be either constant due to a static acceleration, or varying at frequencies up to a few kilohertzs. Thereby, the carrier signal is modulated by the variations of the capacitance. In order to measure the acceleration from the change in the capacitance, several techniques based on synchronous demodulation have been developed [2]. However, as the changes in the capacitance tend

to be extremely small, a highly-linear, low-noise sensor interface is needed. In this work, a differential capacitive bridge has been employed, achieving power supply rejection and cancellation of substrate coupling. In order to operate with a linear displacement-to-voltage behavior and minimize the effects of the parasitic capacitances, a differential transimpedance amplifier (TIA) is proposed. The TIA converts the difference between i_1 and i_2 to a measurable voltage signal, which is then synchronously demodulated and filtered to obtain a signal proportional to the capacitance change, therefore allowing the measurement of the acceleration.

Circuit description

The proposed sensor interface shown in Fig. 2, consists of three main stages: (i) a transimpedance amplifier; (ii) a demodulator; and (iii) a low-pass filter.

This work focuses on the design of the TIA, which is the first and most critical stage in terms of bandwidth, noise and linearity [3]. The carrier frequency that stimulates the sensor depends on the application and the inertial MEMS. In order to optimize the performance of the interface, the bandwidth of the TIA must be adapted to the carrier frequency of each application. If the bandwidth of the TIA is close to the carrier frequency, the signal at the output is not synchronized with the input, and it should be demodulated with a phase-shifted replica of the carrier signal, in order to maximize the sensitivity, which requires a more complex interface. Moreover, as there is a tradeoff between gain, bandwidth and noise, adjusting the gain and bandwidth of the TIA also optimizes the noise performance, allowing the measurement of even smaller accelerations. In this work, we propose a new high gain differential voltage amplifier, implemented, as shown in Fig. 3, with a three

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cascaded differential pairs with load resistors in the first two pairs, and a resistive feedback, in a shunt-shunt topology, which presents low impedance input, high linearity and low noise.

Results

The transimpedance amplifier has been designed in an 180-nm CMOS technology and it is aimed for a capacitive sensor with capacitance up to 1 pF. To carry out the simulations, a 1 pF parasitic capacitance, CP, has been modeled. As shown in Fig. 4, the TIA performs programmable bandwidth from 50 kHz up to 1 MHz, as well as the transimpedance can be modified while keeping a constant bandwidth. At the maximum transimpedance configuration, the TIA presents a minimum equivalent input noise of only 45 fA/ $\sqrt{\text{Hz}}$, that corresponds to a capacitive resolution of 0.02 aF/\sqrt{Hz} at 50 kHz. With a power consumption of only 54 µW, it achieves a maximum sensitivity of 1 mV/fF, or equivalently 3 mV/g for a comb-finger structure of 2-µm gap and 5.5 kHz resonant frequency [4].

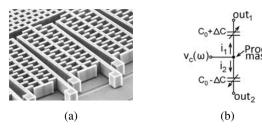


Fig. 1. a) Surface-micromachined comb-finger capacitive accelerometer (STMicroelectronics) and b) its electrical model.

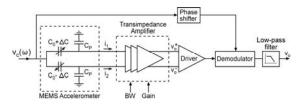


Fig. 2. Conceptual scheme of a capacitance to voltage converter based on transimpedance amplifier and synchronous demodulation. The gain and bandwidth of the transimpedance amplifier are programmed with an externally generated digital word.

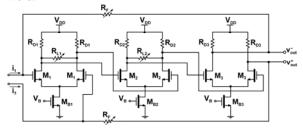


Fig. 3. Schematic circuit of the proposed transimpedance amplifier.

Conclusions

In this work, a fully-differential transimpedance amplifier with programmable gain and bandwidth has been presented. It has been designed in a 180-nm CMOS technology and it is intended for its use in MEMS accelerometers as a capacitive to voltage converter. Although the proposed TIA is aimed for a differential surface-micromachined comb-finger capacitive accelerometer, its programmability allows its use for other inertial capacitive MEMS sensors.

References

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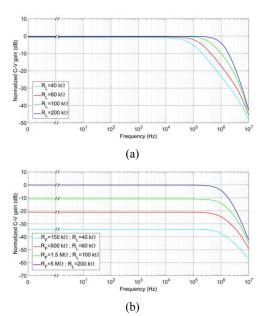


Fig. 4. Programmable frequency response of the TIA with a) variable bandwidth and b) variable gain.